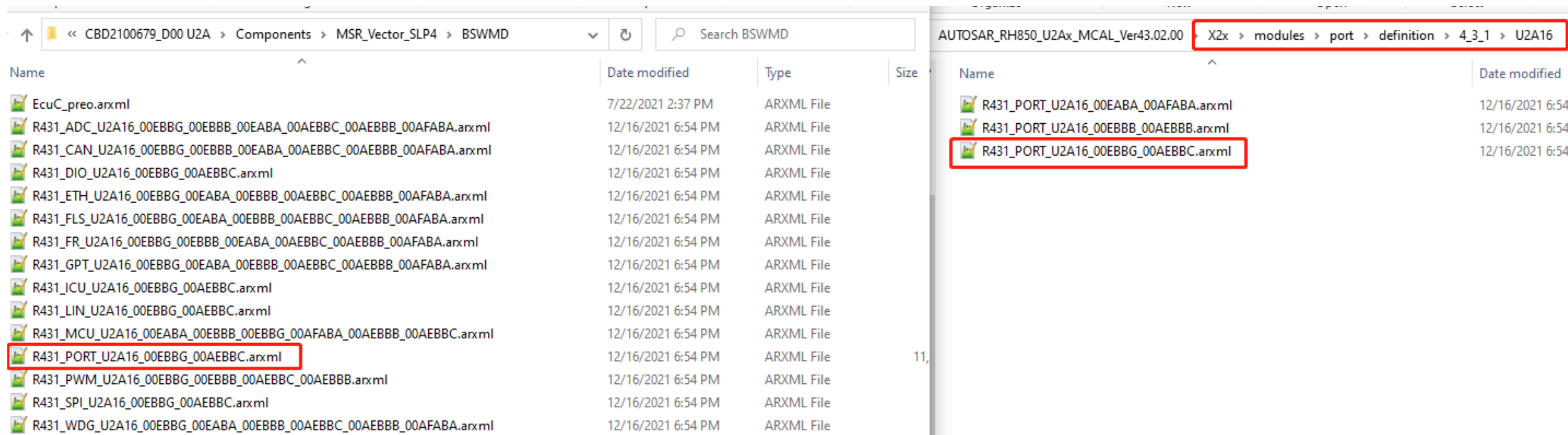


RH850/U SERIES AUTOSAR MCAL GENERATE SOURCE CODE

SOPHIA

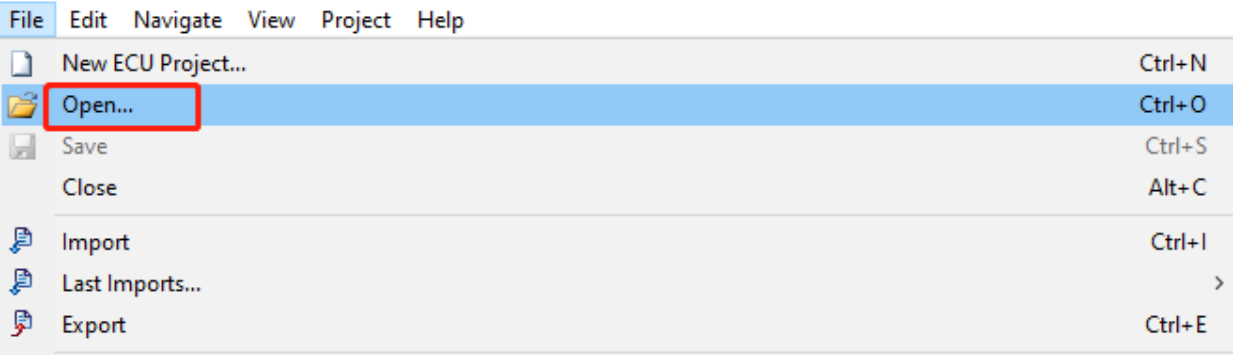
从MCAL PACKAGE 拷贝文件到达芬奇BSWMD文件目录

- 把每个模块下面的这个arxml文件拷贝到vector的这个目录下
- 根据你使用的芯片每个模块只能拷贝一个



打开达芬奇工程

DaVinci Configurator SIP.MCAL - test.dpa - C:\MCAL_DavinciExterGeneration\MCAL_RH850U2A8_V43.02_00_DavinciExt



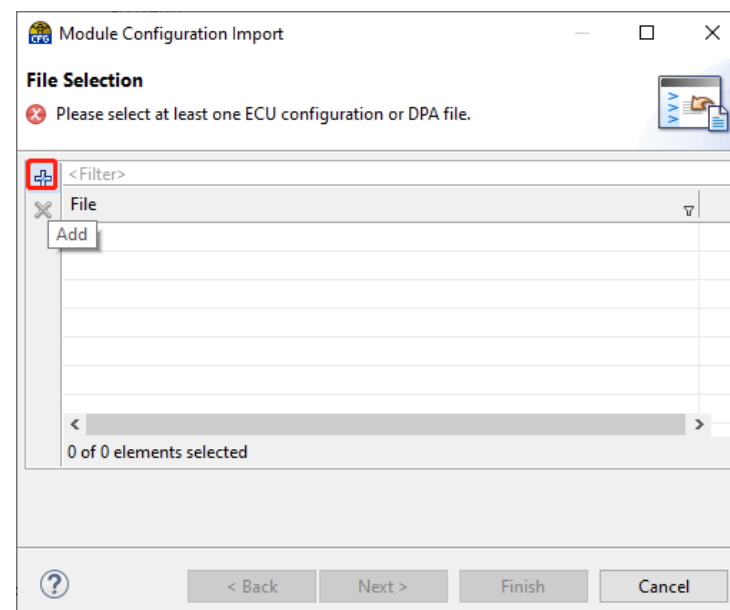
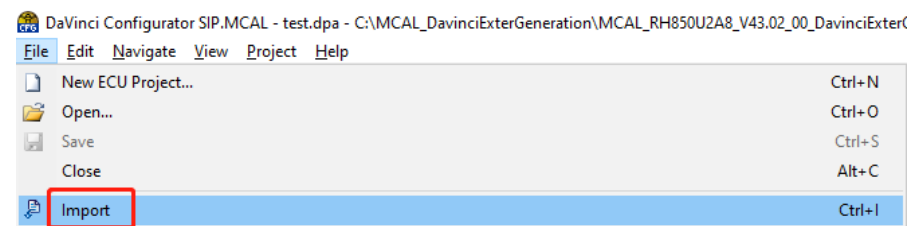
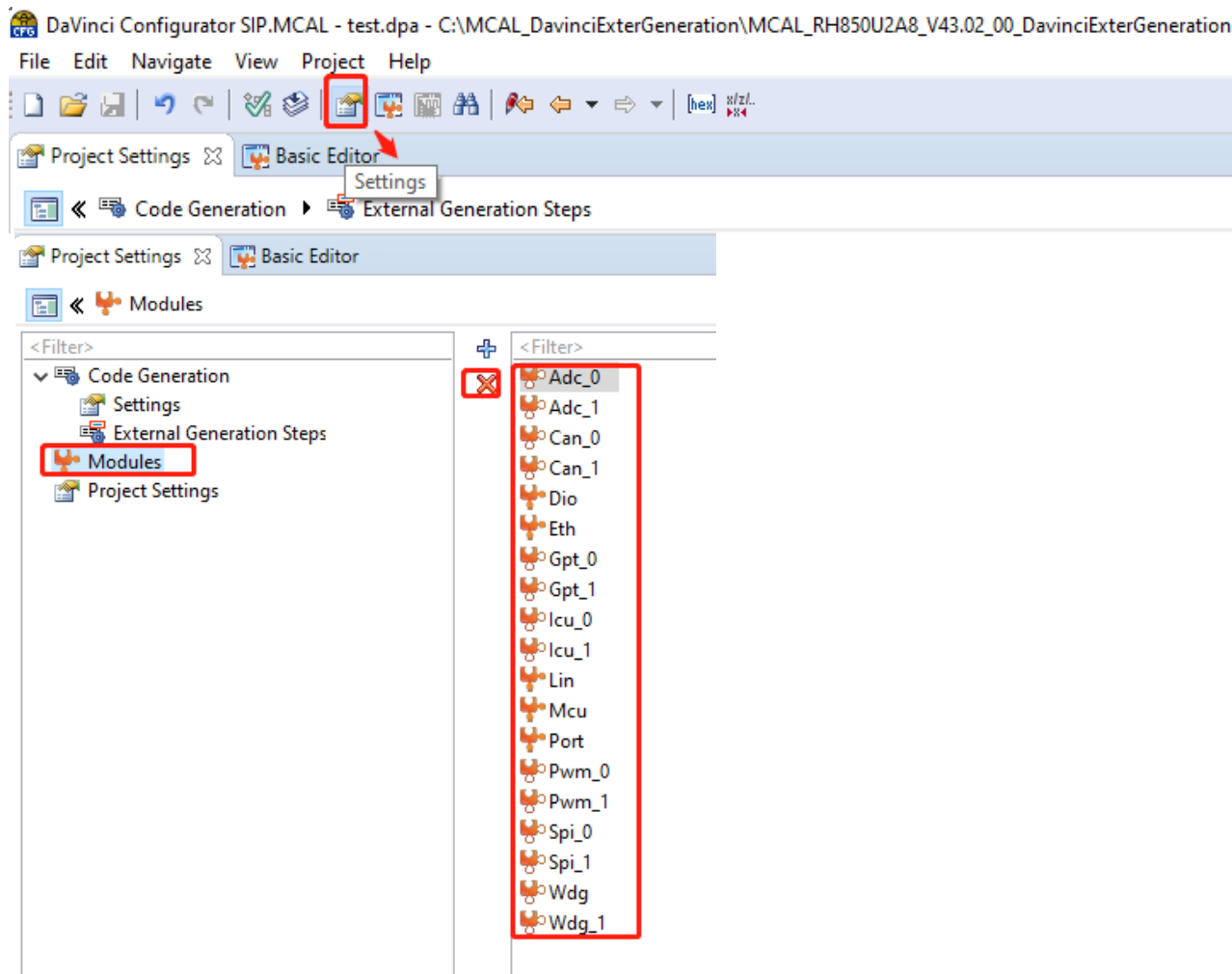
- 打开达芬奇，选择生成包里面的test. dpa工程

Windows > MCAL_DavinciExterGeneration > MCAL_RH850U2A8_V43.02_00_DavinciExterGeneration >

Name	Date modified	Type	Size
Appl	11/25/2022 11:44 AM	File folder	
Config	11/25/2022 11:44 AM	File folder	
DefRestrict	11/25/2022 11:44 AM	File folder	
generator	11/24/2022 10:19 AM	File folder	
Log	11/25/2022 3:14 PM	File folder	
Output	11/25/2022 3:11 PM	File folder	
stubs	11/24/2022 10:19 AM	File folder	
dr7f702301_0.h	12/16/2021 6:54 PM	H File	27,194 KB
test.~lock	11/25/2022 3:14 PM	~LOCK File	0 KB
test.a5137267.silent.dcur	11/25/2022 2:56 PM	DCUSR File	9 KB
test.dpa	11/25/2022 3:14 PM	DPA File	33 KB
Translation.trxml	11/10/2022 5:09 PM	TRXML File	8 KB
U2A8_translation.h	12/16/2021 6:54 PM	H File	354 KB

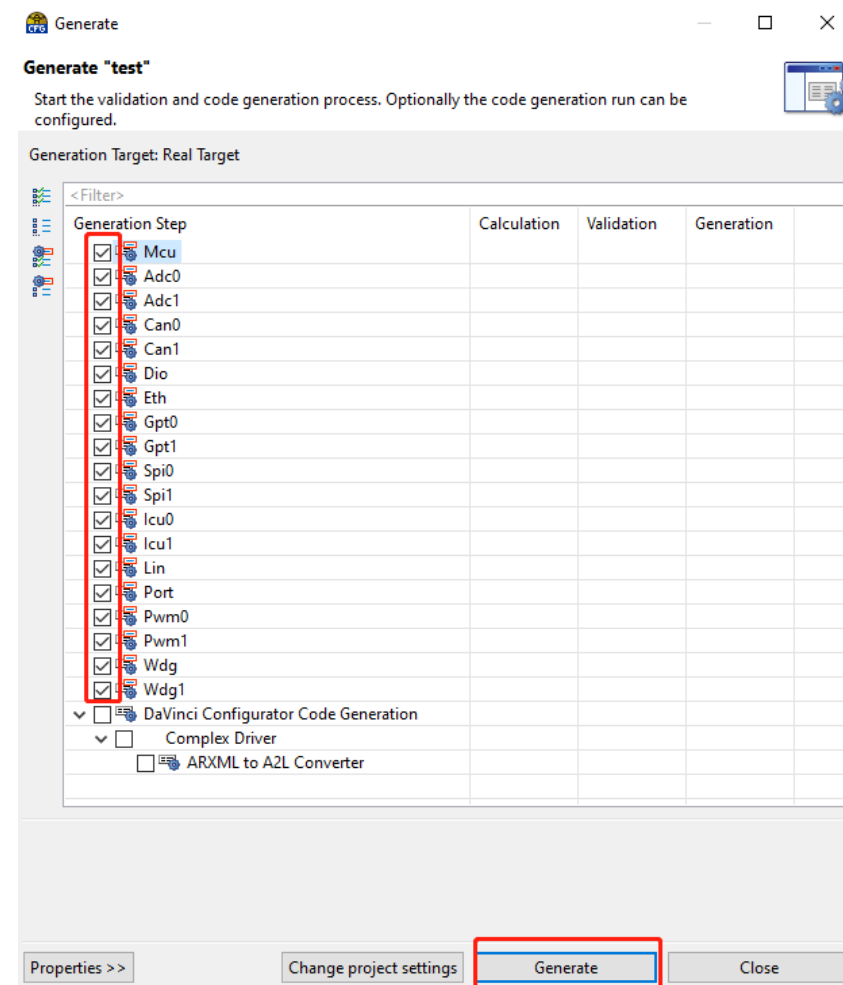
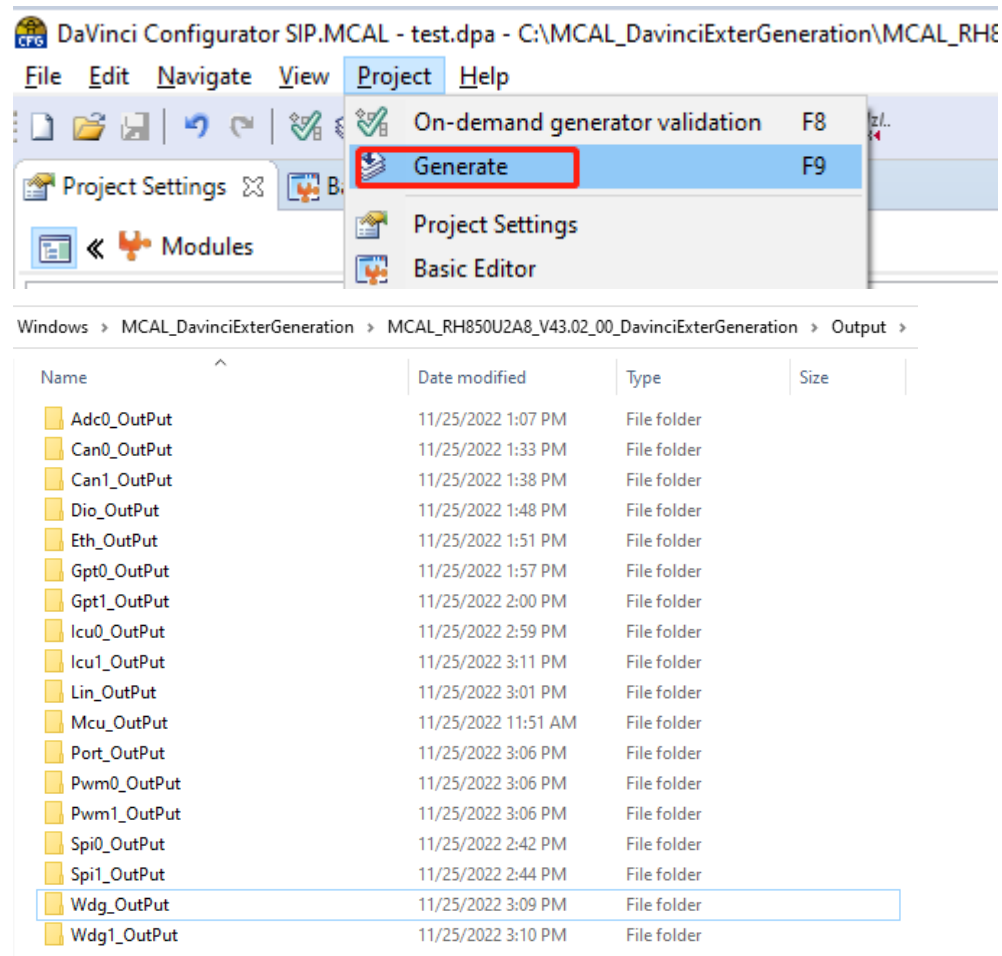
打开SETTING，导入ARXML文件配置修改

- 打开setting，删除工程原有的模块，导入要生成代码的arxml文件，然后保存

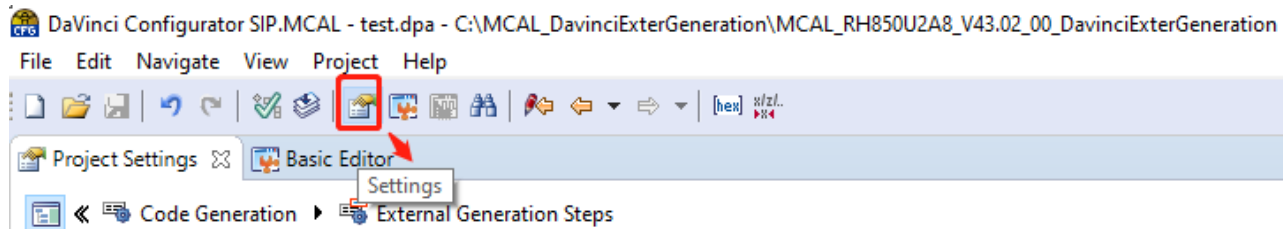


EXTERNAL GENERATION STEPS

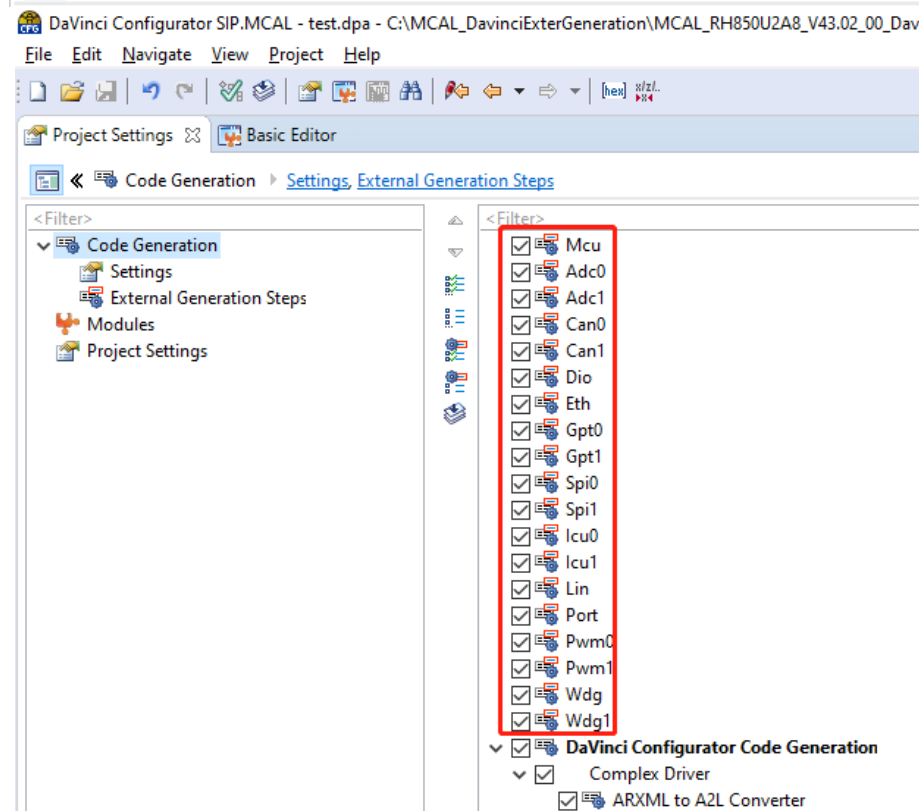
- 生成代码前，先点击保存。点击Generate,选择需要生成代码的模块，然后点击Generate生成代码
- 生成的代码在Output目录下



如需修改，打开SETTING

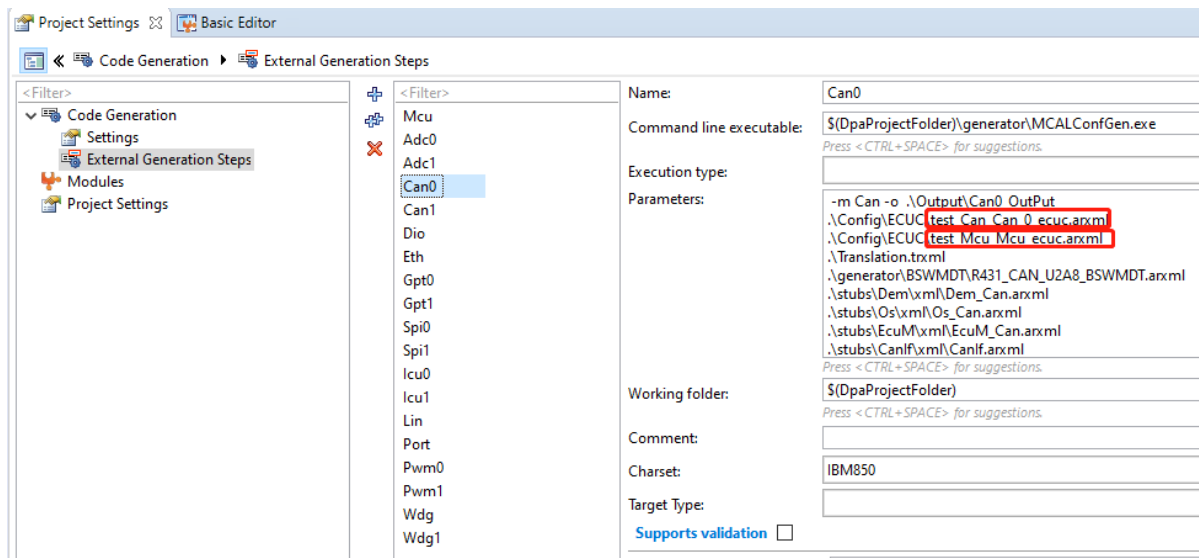


- 打开setting，可以看到已经做好的各模块External Generation Steps



EXTERNAL GENERATION STEPS的配置

- Command line to execute 如果安装路径不一样, 点击后面文件夹图标修改路径
- 如果MCAL_RH850U2A8_V43.02_00_DavinciExterGeneration\Config\ECUC的arxml文件名称和Parameters 不一样, 修改为目录下的名称



Windows > MCAL_DavinciExterGeneration > MCAL_RH850U2A8_V43.02_00_DavinciExterGeneration > Config > ECUC

Name	Date modified	Type	Size
test.ecuc.arxml	11/25/2022 11:48 AM	ARXML File	6 KB
test.ecuc.Initial.arxml	11/25/2022 11:44 AM	ARXML File	1 KB
test_Adc_Adc_0_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	77 KB
test_Adc_Adc_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	73 KB
test_Can_Can_0_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	36 KB
test_Can_Can_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	36 KB
test_Dio_Dio_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	49 KB
test_Eth_Eth_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	76 KB
test_Gpt_Gpt_0_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	22 KB
test_Gpt_Gpt_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	30 KB
test_Icu_Icu_0_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	41 KB
test_Icu_Icu_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	27 KB
test_Lin_Lin_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	11 KB
test_Mcu_Mcu_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	169 KB
test_Port_Port_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	1,225 KB
test_Pwm_Pwm_0_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	52 KB
test_Pwm_Pwm_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	52 KB
test_Spi_Spi_0_ecuc.arxml	11/25/2022 3:12 PM	ARXML File	46 KB
test_Spi_Spi_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	44 KB
test_Wdg_Wdg_1_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	15 KB
test_Wdg_Wdg_ecuc.arxml	11/25/2022 11:48 AM	ARXML File	15 KB

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